FIG.1 THE PRESENT INVENTION

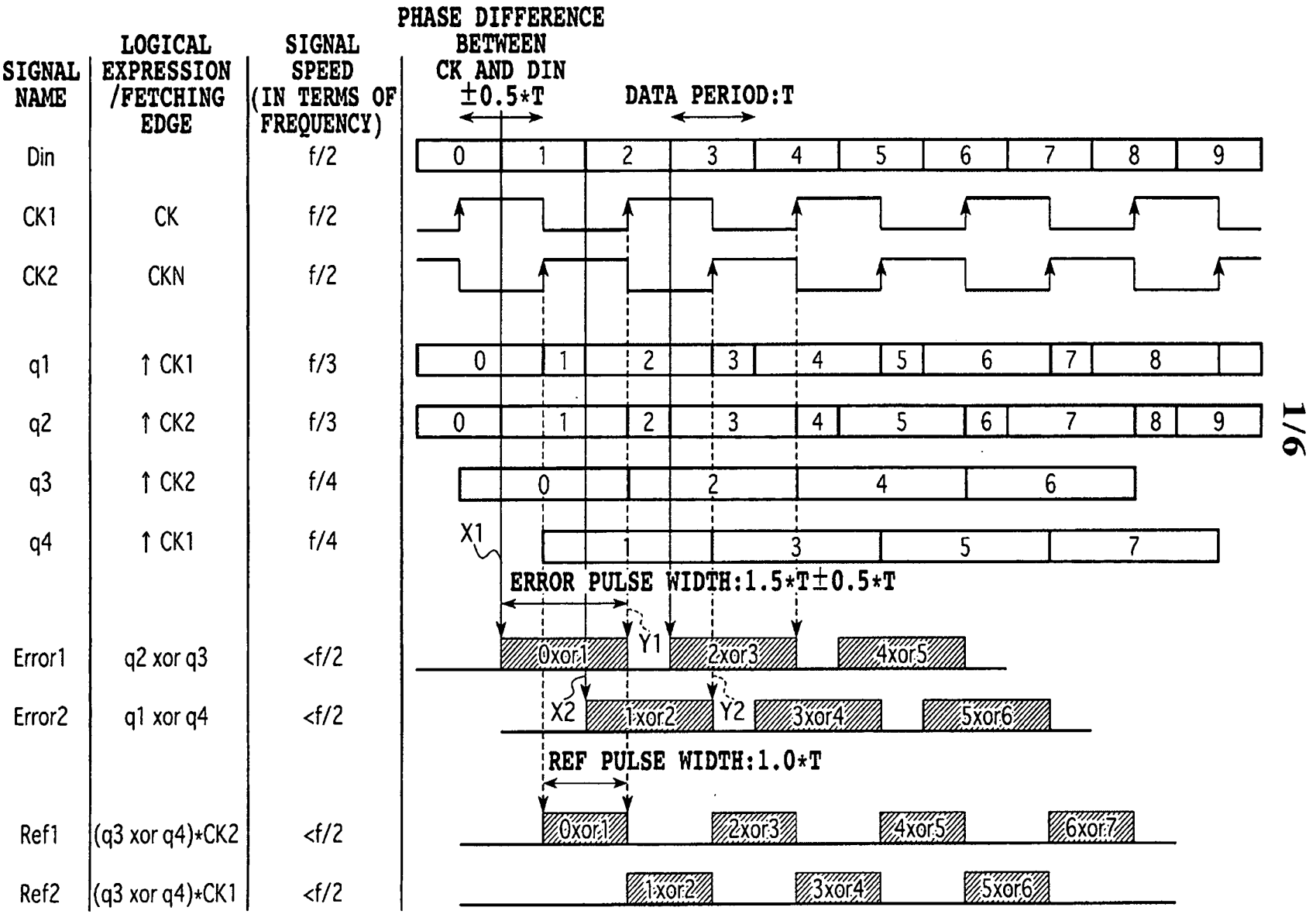

FIG.2 HOLMQVIST AND SUMMERS

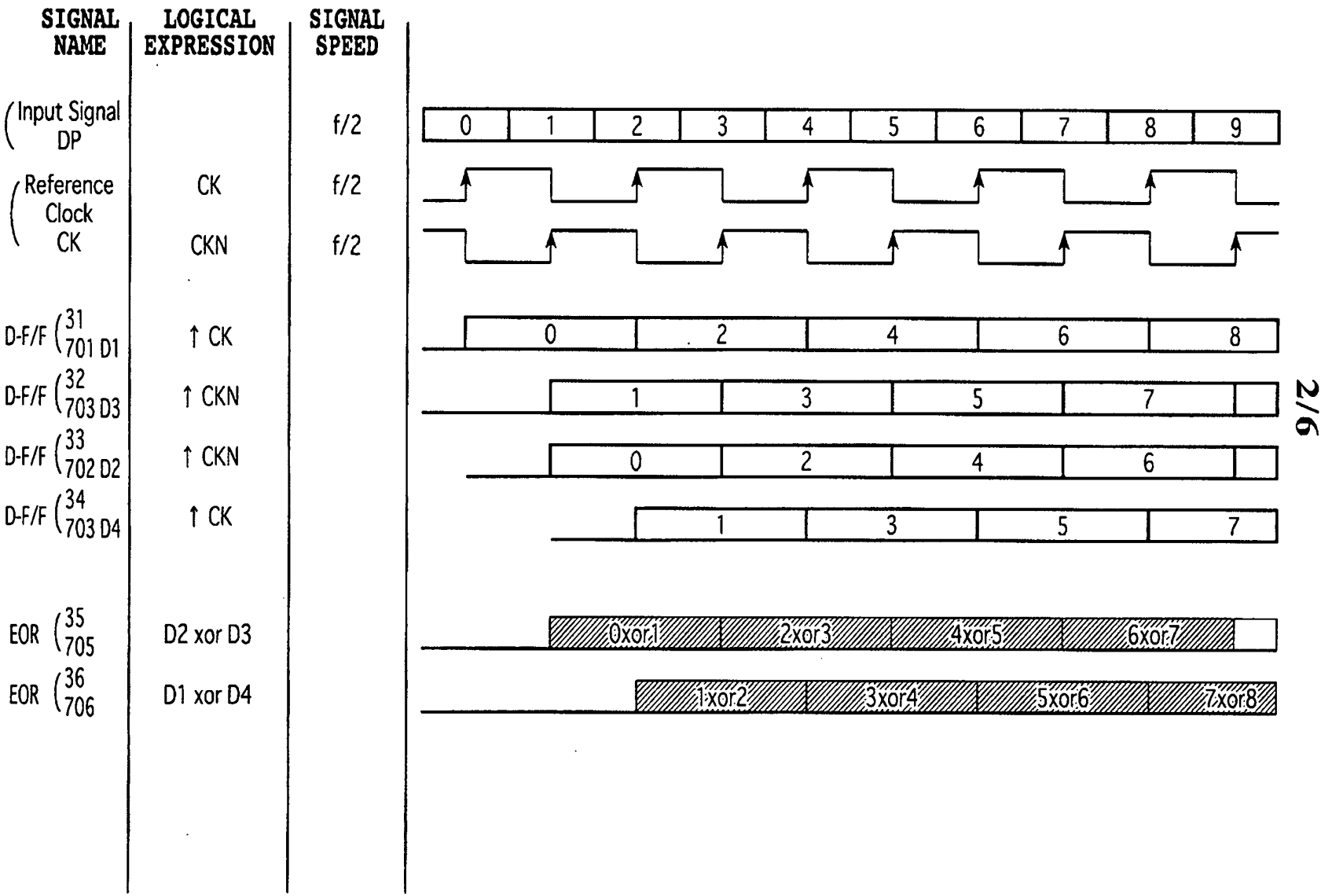

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FIG.3 THE PRESENT INVENTION

SIGNAL NAME	LOGICAL EXPRESSION	SIGNAL SPEED
Din		f/2
CK1	CK	f/2
CK2	CKN	f/2
q1	↑ CK1	
q2	↑ CK2	
q3	↑ CK2	
q4	↑ CK1	
Error1	q2 xor q3	
Error2	q1 xor q4	

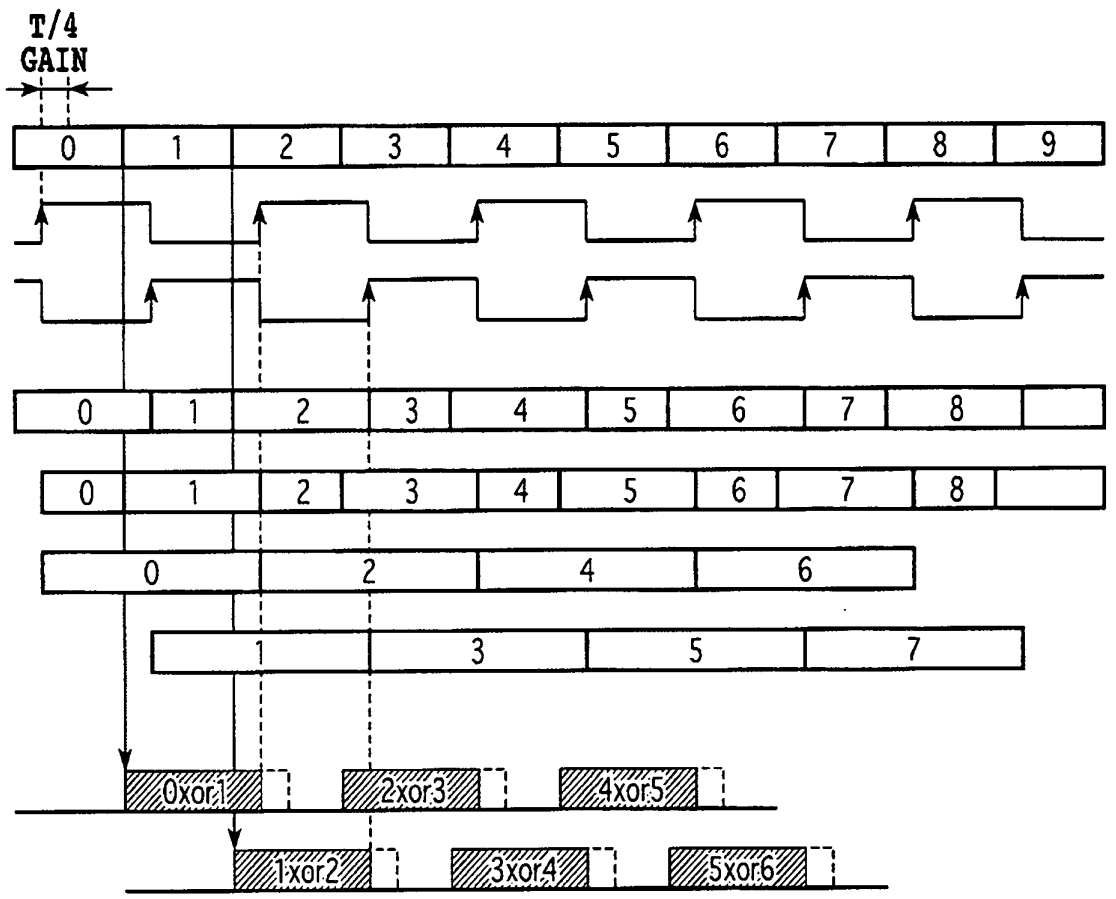

FIG.4 HOLMQVIST AND SUMMERS

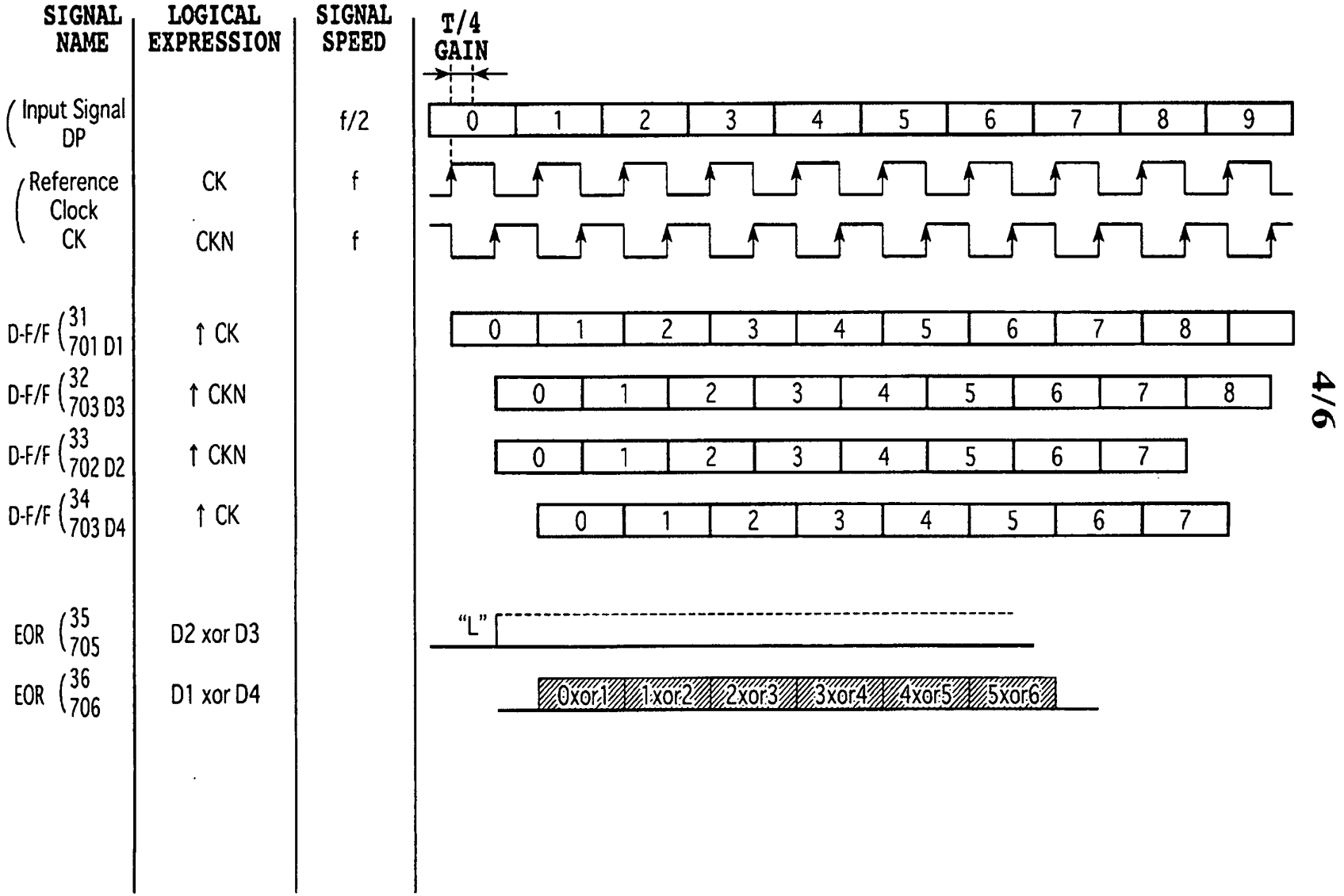

FIG.5 THE PRESENT INVENTION

SIGNAL NAME	LOGICAL EXPRESSION	SIGNAL SPEED
Din		f/2
CK1	CK	f/2
CK2	CKN	f/2
q1	↑ CK1	
q2	↑ CK2	
q3	↑ CK2	
q4	↑ CK1	
Error1	q2 xor q3	
Error2	q1 xor q4	

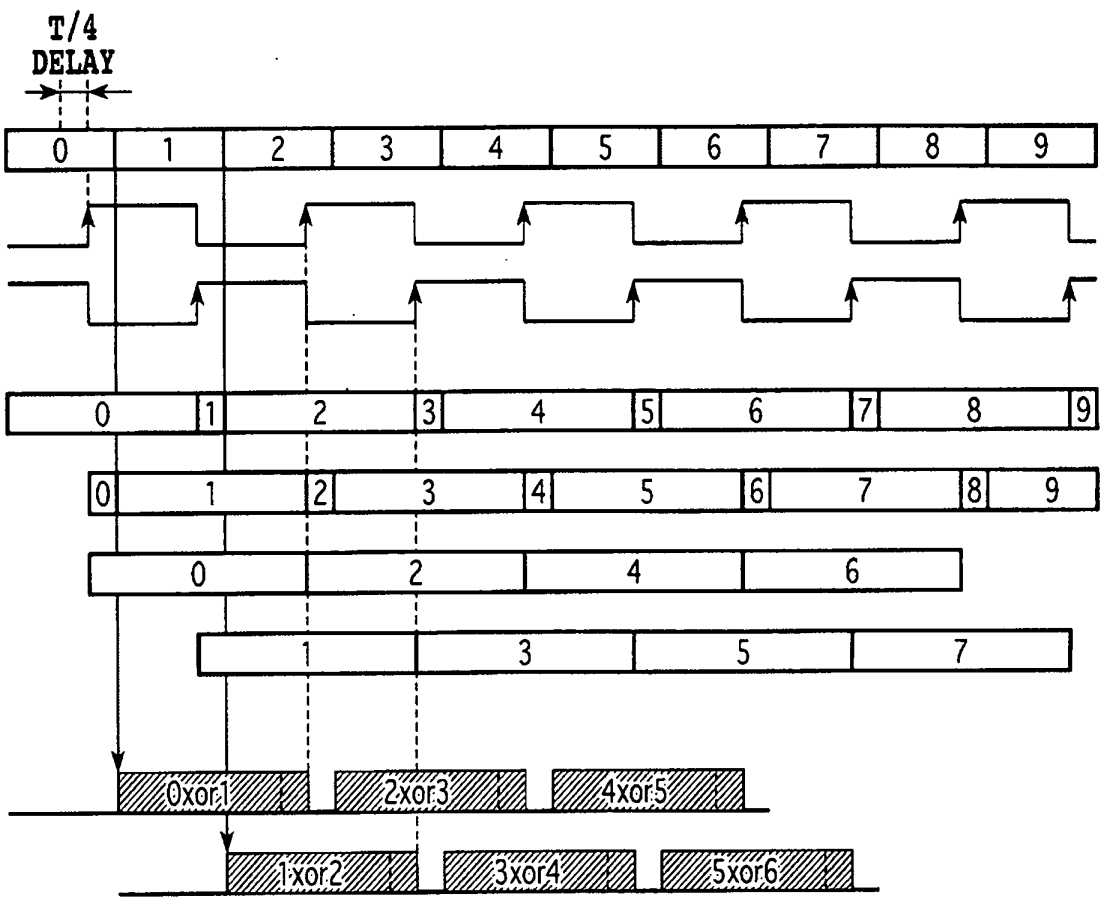

FIG.6 HOLMQVIST AND SUMMERS

SIGNAL NAME	LOGICAL EXPRESSION	SIGNAL SPEED
Input Signal DP		f/2
Reference Clock CK	CK	f
	CKN	f
D-F/F (31 701 D1	↑ CK	
D-F/F (32 703 D3	↑ CKN	
D-F/F (33 702 D2	↑ CKN	
D-F/F (34 703 D4	↑ CK	
EOR (35 705	D2 xor D3	
EOR (36 706	D1 xor D4